

APPENDIX A
"CLEAN" VERSION OF EACH PARAGRAPH/SECTION/CLAIM
37 C.F.R. § 1.121(b)(ii) AND (c)(i)

SPECIFICATION:

The paragraph beginning at page 1, line 5:

91 The present invention relates to a PLL frequency synthesizer, and more particularly to a PLL frequency synthesizer for driving a charge pump using an output from a phase comparator for comparing a phase of a frequency of a generation voltage of a voltage-controlled oscillator with a phase of a reference frequency, and driving the voltage-controlled oscillator using an output from the charge pump, thereby outputting a signal having a set desired frequency.

The paragraph beginning at page 1, line 15:

92 A generally used PLL frequency synthesizer drives a charge pump using an output from a phase comparator, and drives a VCO using an output from the charge pump. The charge pump can be driven by various methods. The mainstream current drains or absorbs current to or from the charge pump in accordance with an output from the phase comparator.

The paragraph beginning at page 2, line 8:

93 In the frequency synthesizer disclosed in Japanese Unexamined Patent Publication No. 10-107628, the power supply of the phase comparator is controlled in the above manner. However, the phase comparator itself is integrated into an IC, so it is not practical in consideration of the current popularity of synthesizer ICs to control the power supply of only the phase comparator.

The paragraph beginning at page 11, line 6:

94 Power supply voltage setting of the VCO 6 by the VCO power supply voltage setting device 9 assumes a continuously changeable variable resistor. However, the output frequency may exhibit discrete changes such as small, medium, and large changes.